CPE301 – SPRING 2022

Design Project – PCB Design

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Primary Github address: <https://github.com/DylanCaz/Submission_DA>

Directory: <https://github.com/DylanCaz/Submission_DA/tree/main/KiCad%20PCB%20Design%20Project>

1. A picture containing chart

   Description automatically generated**COMPONENTS LISTS AND CONECTION BLOCK DIAGRAM w/ PINS**
2. Diagram, schematic

   Description automatically generated**SCHEMATIC**
3. **PCB VIEW**

Diagram, schematic

Description automatically generated

DRC Checker

Graphical user interface, text, application, email

Description automatically generated

1. **3D VIEW**

A picture containing text, electronics, circuit

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1. **GERBER VIEW**

Diagram, schematic

Description automatically generated

1. **GITHUB LINK OF THIS DA**

<https://github.com/DylanCaz/Submission_DA/tree/main/KiCad%20PCB%20Design%20Project>

**Student Academic Misconduct Policy**

<http://studentconduct.unlv.edu/misconduct/policy.html>

“This assignment submission is my own, original work”.

Dylan Cazares